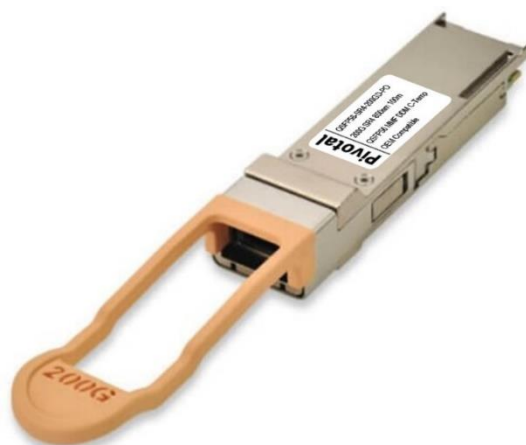


QSFP56-SR4-200GD

200GBASE, QSFP56, SR4, MMF TRANSCEIVER 850nm, 70m REACH, MPO-12 UPC CONNECTOR

Product Features

- Up to 50Gbps data rate per channel by PAM4 modulation
- 4 parallel lanes on 850nm center wavelength
- MPO-12 UPC connecting interface compliant.
- Single +3.3V power supply
- DDM function implemented.
- Hot-pluggable QSFP56 form factor
- Length up to 70/100m over OM3/OM4 with FEC
- Max power dissipation: 5w
- Compliant with ROHS10
- International class 1 laser safety certified
- Standards:
 - IEEE 802.3cd 200GBASE-SR4 Compliant
 - CMIS Rev 4.0
 - QSFP56 MSA Compliant
- Case operating temperature:
 - Commercial: 0 ~ 70°C



Product Applications

- 200GBASE-SR4
- 100GBASE-SR4
- 40GBASE-SR4
- 2x100GBASE-SR2
- 4x50GBASE-SR
- 4x25GBASE-SR
- 4x10GBASE-SR

I. Maximum Ratings

Exceeding the limits below may damage the transceiver module permanently.

Parameter	Symbol	Min.	Typ.	Max.	Units
Storage Temperature Range	Ts	-40		+85	°C
Power Supply Voltage	Vcc	-0.5		3.6	V
Relative Humidity (non-condensation)	RH	0		85	%

II. Operating Specifications

Parameter	Symbol	Min.	Typ.	Max.	Units	Notes
Case Operating Temperature	TC	0		+70	°C	Commercial
Power Supply Voltage	V _{cc}	3.135	3.3	3.465	V	
Baud Rate (per channel)	BR		26.5625		GBd	
Link Distance (SMF)	D			70/100	m	OM3/OM4

III. Optical Characteristics

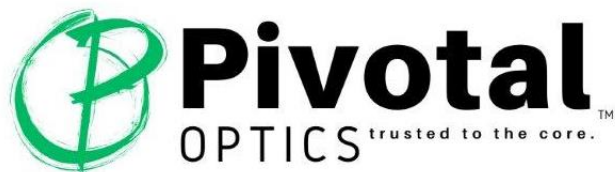
Parameter	Symbol	Min	Typical	Max	Unit	Notes
Center Wavelength (range)	λ_c	840	850	860	nm	
Signaling Speed (each lane)			26.5625		GBd	
Transmitter (per lane)						
Modulation Format			PAM4			
Side-Mode suppression ratio	SMSR	30				
Average Launch Power (each lane)	P _{avg}	-6.5		4	dBm	
Tx OMA (per lane)	TxOMA	-4.5		3	dBm	
Extinction Ratio	ER	3			dB	
Transmitter and Dispersion eye closure for PAM4 (per lane)	TDECQ			4.5	dB	
Optical Return Loss Tolerance	TOL			12	dB	
Receiver (per lane)						
Damage Threshold	DT	5			dBm	
Average Receive Power (per lane)	RXP	-8.4		4	dBm	
Receive power OMA (per lane)				3	dBm	
Receiver Sensitivity (OMA _{outer})				-6.5	dBm	@2.4e-4
Stressed Receiver Sensitivity (OMA _{outer})				-3.4	dBm	@2.4e-4
Receiver Reflectance	R _{fl}			-12	dB	

IV. Electrical Characteristics

Parameter	Symbol	Min.	Typ.	Max.	Unit	Notes
Power Consumption	P			6	W	
Supply Current	Icc			1.81	A	
Signaling Rate (each lane)	TP1 & TP4	26.5625 ± 100 ppm			GBd	TX & RX
Transmitter (each lane)						
Differential pk-pk Input Voltage Tolerance	TP1a	900			mVpp	1
Differential termination mismatch	TP1			10	%	
Differential input return loss	TP1	IEEE 802.3-2015 Equation (83E-5)			dB	
Differential to Common Mode Input Return Loss	TP1	IEEE 802.3-2015 Equation (83E-6)			dB	
Module Stressed Input Test	TP1a	See IEEE 802.3bs 120E.3.4.1				2
Single-ended Voltage Tolerance Range (Min)	TP1a	-0.4 to 3.3			V	
DC Common Mode Input Voltage	TP1	-350		2850	mV	3
Receiver (each lane)						
Differential pk-pk Output Voltage	TP4			900	mVpp	
Common Mode Voltage		-350		2850	mV	
AC Common Mode Output Voltage, RMS	TP4			17.5	mV	
Differential Termination Mismatch	TP4			10	%	
Differential Output Return Loss	TP4	IEEE 802.3-2015 Equation (83E-2)				
Common to Differential Mode Conversion Return Loss	TP4	IEEE 802.3-2015 Equation (83E-3)				
Transition Time, 20% to 80%	TP4	9.5			ps	
Near-end Eye Symmetry Mask Width (ESMW)	TP4		0.265		UI	
Near-end Eye Height, Differential	TP4	70			mV	
Far-end Eye Symmetry Mask Width (ESMW)	TP4		0.2		UI	
Far-end Eye Height, Differential	TP4	30			mV	
Far-end Pre-cursor ISI Ratio	TP4	-4.5		2.5	%	
Common Mode-Output Voltage	TP4	-350		2850	mV	3

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Notes:

1. With the exception to IEEE 802.3bs 120E.3.1.2 that the pattern is PRBS31Q or scrambled idle.
2. Meets BER specified in IEEE 802.3bs 120E.1.1.
3. DC common mode voltage generated by the host. Specification includes effects of ground offset voltage.

V. Ordering Information

Pivotal Part Number	Description
QSFP56-SR4-200GD	QSFP56, 850nm, SR4 MMF MPO-12 100m, 200G DDM, C-Temp

Notes:

1. Please contact sales for specific OEM Platform part numbers to fit your network.

Warranty

<https://pivotaloptics.com/warranty/>

Disclaimer

External physical characteristics are subject to variation. This may include, but is not limited to, external case designs, pull tab colors and/or shapes, removal latch styles or colors, and label sizes and placement. These variations do not affect the function or characteristics of the transceivers.